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APPLICANTS Takashi Yoneda, Osaka, JAPAN; Tsutomu Kamiyoshi, Nara, JAPAN; Hiroshi Benno, Osaka, JAPAN; Shirou Yoshioka, Hyogo, JAPAN; Tsuneo Uenishi, Hyogo, JAPAN;				
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged Examiner's Signature		<input checked="" type="checkbox"/> Yes <input type="checkbox"/> no <input type="checkbox"/> Yes <input type="checkbox"/> no Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 3
ADDRESS Jack Q. Lever, Jr. McDERMOTT, WILL & EMERY 600 Thirteenth Street, N.W. Washington, DC 20005-3096		TOTAL CLAIMS 11	INDEPENDENT CLAIMS 1	
TITLE Semiconductor integrated circuit				
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